Listing of Claims:

This listing of claims replaces all prior versions and listings of claims in the application.

Amendments to the Claims

1. (currently amended) An interface for an electronic device being coupled to a peripheral device, the interface comprising:

a first configurable hardware interface, wherein the configurable hardware interface is resident in the electronic device and includes:

a first programmable logic device (PLD);

a first memory coupled to the first PLD;

a first control interface for controlling the first PLD and the first memory; and

a first communication interface for receiving information from the peripheral device and enabling the first control interface, the first communication interface adapted to request a bitstream from the peripheral device responsive to a signal from the first control interface; ~~and~~

a first storage component for storing a plurality of bitstreams that configure the first configurable hardware interface to implement a driver of the peripheral device;~~.~~

a second configurable hardware interface, wherein the configurable hardware interface is resident in the peripheral device and includes:

a second PLD;

a second memory coupled to the second PLD;

a second control interface for controlling the second PLD and the second memory; and

a second communication interface for receiving information from the electronic device and enabling the second control interface, the second communication interface adapted to request a bitstream from the electronic device responsive to a signal from the second control interface; and

a second storage component for storing a plurality of bitstreams that configure the second configurable hardware interface to implement an interface compatible with the driver on the first configurable hardware interface.

2. (currently amended) The interface of Claim 1, wherein at least one of the first and second storage components includes volatile memory.

3. (currently amended) The interface of Claim 1, wherein at least one of the first and second storage components includes static random access memory.

4. (currently amended) The interface of Claim 1, wherein at least one of the first and second communication interfaces includes one of a universal serial bus, a parallel port connector, a serial port connector, and a small computer system interface (SCSI).

5. (currently amended) The interface of Claim 1, wherein the first and second communication interfaces establish~~es~~ synchronous communication between the electronic device and the peripheral device.

6. (currently amended) The interface of Claim 1, wherein at least one of the first and second memories ~~memory~~ includes at least one lookup table.

7. (previously presented) The interface of Claim 1, further including at least one of an Ethernet interface, a modem interface, and a custom interface for communicating with the peripheral device.

8. (currently amended) A hardware implemented method of facilitating communication between two devices, the method comprising:

identifying a host device, from the two devices, that controls communication between the two devices;

identifying a peripheral device that accepts commands from the host device;

storing a plurality of bitstreams in the host device, the plurality of bitstreams corresponding to drivers;

determining whether one of the drivers is a driver of the peripheral device,

wherein if one of the drivers is the driver of the peripheral device, then selecting that bitstream corresponding to the driver of the peripherals device,

otherwise, directing the host device to receive a first bitstream from the peripheral device; ~~and~~

configuring a first programmable logic device (PLD) in the host device with the first bitstream to implement the driver of the peripheral device; and

configuring a second PLD in the peripheral device with a second bitstream that implements an interface compatible with the driver implemented in the first PLD.

9. (currently amended) The hardware implemented method of Claim 8, further including storing a plurality of designations in the first PLD, wherein each designation corresponds to one of the plurality of bitstreams, wherein determining includes searching the plurality of designations.

10. (previously presented) The hardware implemented method of Claim 9, wherein the plurality of designations are stored in at least one lookup table.

11. (currently amended) The hardware implemented method of Claim 9, wherein each designation includes an address for one of the plurality of bitstreams stored in the host device, and wherein selecting includes accessing an address in the host device for the first bitstream to implement the driver of the peripheral device.

12. (currently amended) A method for configuring an interface, comprising:

communicating a designation of a driver ~~identifier~~ from a first device to a second device;

determining at the second device whether a first configuration bitstream associated with the designation of the driver ~~identifier~~ is stored in storage of the second device;

communicating a bitstream request from the second device to the first device in response to the first bitstream being absent from the storage;

transmitting, in response to the bitstream request, the first bitstream from the first device to the second device; ~~and~~

configuring a first programmable logic device (PLD) on the second device with the first bitstream; and

configuring a second PLD in the first device with a second bitstream that implements an interface compatible with a driver implemented in the first PLD.

13. (currently amended) The method of claim 12, further comprising, in response to the first bitstream being present in the storage, reading the first bitstream from the storage and configuring the first PLD on the second device with the first bitstream.

14. (previously presented) The method of claim 13, further comprising storing the first bitstream received from the first device in the storage on the second device.

15. (currently amended) The method of claim 12, further comprising storing a plurality of configuration bitstreams and associated designations of drivers ~~identifiers~~ in the storage.